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... The HAT format is used both in main **memory** and **cache** ... A **cache** line could contain one or more bundles. ... the PC is incremented as usual, but after **fetching** the last ...

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... The hardware looks ahead by **fetching** instructions into a buffer ... FP (32Kb 4Way) **Cache** Data Slot **Memory** Reg File ... table cor- responding to the load **cache** miss this ...

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... To control **fetching** from the protocol thread, we ... thread shares the instruc- tion **cache** with the ... in an unmappedportion of the physical **memory**, the protocol ...

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... Springer-Verlag Berlin Heidelberg 2001 ... **memory** system, rather than early in the instruction-execution ... The hardware stops **fetching** user-level instructions (light ...

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Minding network queues with FPGAs and memory

... Each **queue** will have a single entry in the **memory** block that uses status flags, **head pointers** (read) and **tail pointers** (write) to describe the **queue**. ...

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When we looked at the ordered list and stack data structures, we ...

... would stop the **queue** from traversing the entire **memory**, but it would only allow us to fill the **queue** one time. Once the **head** and **tail pointers** reached the stop ...
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C Programming Tutorial: Linked Lists, Trees, Hash Tables

... **tmp = head->next; free(head); head = tmp;** } } Now ... Think of a typical (non-priority) printer **queue**: The first ... By creating a **tail pointer** you can keep track of ...

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The Next Generation of Intel IXP Network Processors

... these reference 1 of 16 data store **tail** or **head pointers**. ... enqueue writes the address of the **pointer** to be ... to the address of the cached **tail pointer**, and then ...
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Exercises 4: Linked-List Queue Management (U.Crete, CS-534)

... is stored in the same word with the **queue head pointer**, and the other is stored in the same word with the **queue tail pointer**. The counts of **memory** accesses are ...
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... Determine the above 4 counts of **memory** accesses separately in ... in the same word where the **queue head pointer** is stored. Empty with Tail: the **queue-empty** flag is ...
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... The **head pointer** stores the address location for the next read for the **queue** in the external **memory**. The **tail pointer** stores the address location for the next ...
www.altera.com/literature/wp/wp_queue_manager.pdf - Similar pages

malloc question for queue

... How can i say to to that pointer that points that tail of my queue, to return back to the head of the queue? (cyclic queue) Thanks for any ideas. ...

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... 8-23. Figure 8.11 A queue implementation with **head** and **tail** pointers. Note how the queue crawls through **memory** as entries are inserted and removed. 8-24. ...

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